



*JF*  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Mu-Jing Li, et al.

Title: METHOD AND APPARATUS FOR EFFICIENTLY LOCATING AND AUTOMATICALLY CORRECTING CERTAIN VIOLATIONS IN A COMPLEX EXISTING CIRCUIT LAYOUT

Application No.: 10/686,471      Filed: October 15, 2003

Examiner: Do, Thuan V.      Group Art Unit: 2825

Atty. Docket No.: 004-8063

---

May 13, 2005

COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR STATUS OF APPLICATION**

Dear Sir:

Applicant(s) hereby request a report on the status of the above-identified patent application. If a telephone call would expedite this request, please call the undersigned at the number shown below.

Respectfully submitted,

Andrew C. Graham, Reg. No. 36,531  
Attorney for Applicant(s)  
(512) 338-6313  
(512) 338-6301 (fax)